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TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT (Under 37 CFR 1.97(b) or 1.97(c))		Docket No. BUR920030183US1	
In Re Application Of: JOHN E. BARTH JR.			
Serial No. 10/708917	Filing Date 03/31/04	Examiner	Group Art Unit
Title: BITLINE TWISTING FOR MEMORY ARRAYS INCORPORATING REFERENCE WORDLINES			
<p>Address to: Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450</p> <p>37 CFR 1.97(b)</p> <p>1. <input checked="" type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.</p> <p>37 CFR 1.97(c)</p> <p>2. <input type="checkbox"/> The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:</p> <p><input type="checkbox"/> the statement specified in 37 CFR 1.97(e);</p> <p>OR</p> <p><input type="checkbox"/> the fee set forth in 37 CFR 1.17(p).</p>			

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BITLINE TWISTING FOR MEMORY ARRAYS INCORPORATING REFERENCE WORDLINES

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(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

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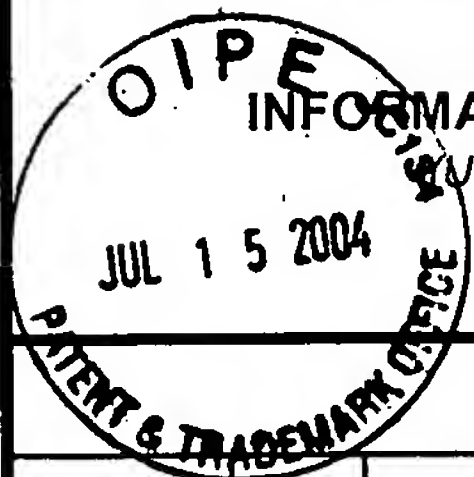
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10/708917

JOHN E. BARTH ET AL.

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03/31/04

GROUP

U.S. PATENT DOCUMENTS

*EXAMINER *INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

		1	Ad. J. van de Goor and Ivo Schanstra; "Address and Data Scrambling: Causes and Impact on Memory Tests;" Proceedings of the First IEEE International Workshop on Electronic Design, Test and Applications; 2002

EXAMINER

DATE CONSIDERED

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.